

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE

SCANNED AC3 Q.A. ~~LA~~

APPLICATION NO. 09/976490	CONT/PRIOR	CLASS 714	SUBCLASS 733	ART UNIT 2133	EXAMINER D
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NTS Michael Dorsey

## TRIMMINGS

**TITLE**      **APPLICANTS**

## ASIC BIST controller employing multiple clock domains

PTO-204D  
12/89

## ISSUING CLASSIFICATION

[illegible]

<b>DISCLAIMER</b> <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<b>DRAWINGS</b> <div> <div>Sheets Drawn</div> <div>Figs. Drawn</div> <div>Print Fig.</div> </div>			<b>CLAIMS ALLOWED</b> <div> <div>Total Claims</div> <div>Print Claim for O.G.</div> </div>	
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